
LabVIEW NXG FPGA Module

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LabVIEW NXG 5.1 FPGA Module Manual

Use the LabVIEW NXG FPGA Module to create an FPGA application with Clock-Driven Logic code, debug your FPGA code, and integrate the application into your system for use with NI FPGA hardware. Use this manual to find programming concepts, step-by-step instructions, and reference information about using the LabVIEW NXG FPGA Module.



Tip For interactive lessons on programming FPGAs, visit the **Learning** tab in software.

Top Tasks

- [Integrate and test your FPGA code](#)—Integrate your FPGA code and test your FPGA application or pieces of the application to ensure that the application functions as expected.
- [Compile your FPGA code](#)—Compile your FPGA code into a bitfile that you then deploy to an FPGA target.
- [Download and run an FPGA VI](#)—Deploy your FPGA code to an FPGA target by downloading and running the bitfile on the FPGA.

Programming Reference

- [Clock-Driven Logic](#)

New Features and Changes

Refer to the list below to learn what's new or changed in the LabVIEW NXG 5.1 FPGA Module.

New Features

- Support for CompactRIO FPGA targets—Use the new [I/O Module Properties](#) node

and [Module Constant](#) to view properties for certain CompactRIO devices.